



SS510LAD

Low Forward-biased Trench Schottky Diode

Features

- High ESD resistance
- Low positive polarity
- High surge current resistance capability

Applications

- SMPS
- High Frequency Inverter
- Polarity Protection Circuit and Other Various Electronic Circuit on the board

Absolute Maximum Ratings

($T_A=25^{\circ}\text{C}$, unless otherwise specified)

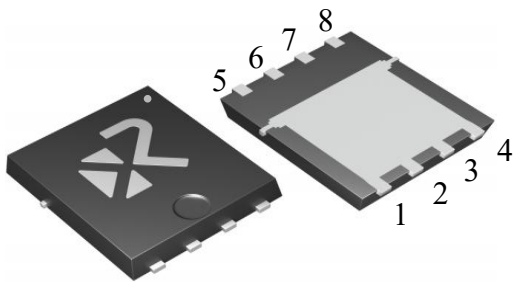
Parameter	Symbol	Limit	Unit
Peak repetitive reverse voltage	V_{RRM}	100	V
Average rectified output current ⁽¹⁾	I_O	5	A
Peak Forward Surge Current@8.3ms	I_{FSM}	80	A
Maximum Effective(Equivalent) Junction Temperature	T_{VJ}	150	$^{\circ}\text{C}$
Storage Temperature Range	T_{STG}	-40~150	$^{\circ}\text{C}$

Note:(1)Tested pulse width $t_p < 5\text{ms}$

Electrical Characteristics ($T_A=25^{\circ}\text{C}$, unless otherwise specified)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Breakdown voltage	VBR	$I_R=100\mu\text{A}$	100			V
Reverse current	I_R	$V_R=100\text{V}$			10	μA
Forward voltage	VF	$I_F=1\text{A}$			0.48	V
		$I_F=2\text{A}$			0.55	V
		$I_F=3\text{A}$			0.62	V
		$I_F=5\text{A}$			0.75	V

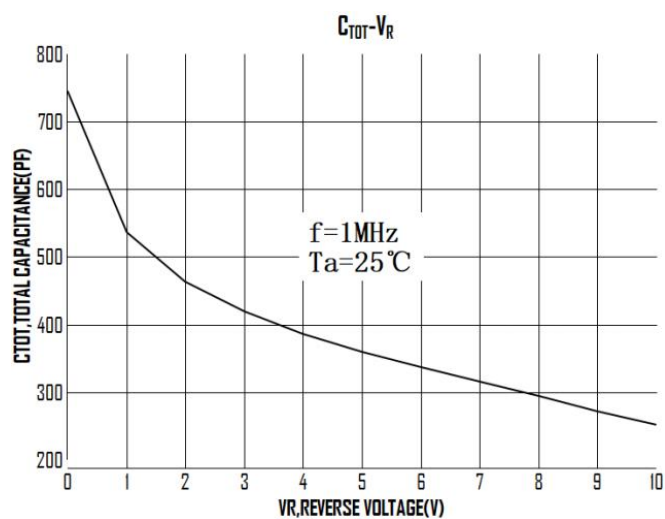
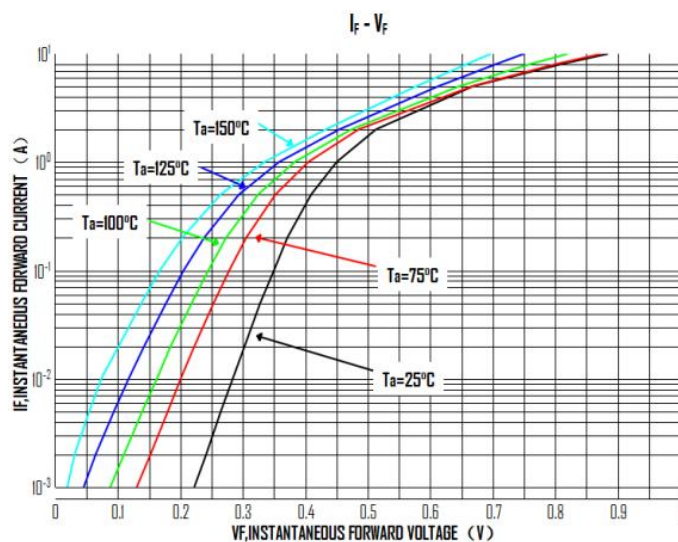
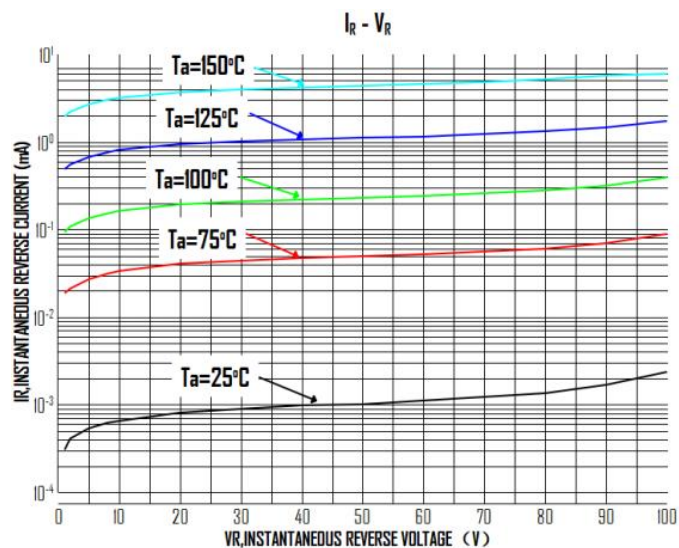
Pin Configuration	
Pin NO.	Pin Name
1,2,3,4	Cathode
5,6,7	Anode
8	Floating



PDFN 5*6-8L



Typical Characteristics



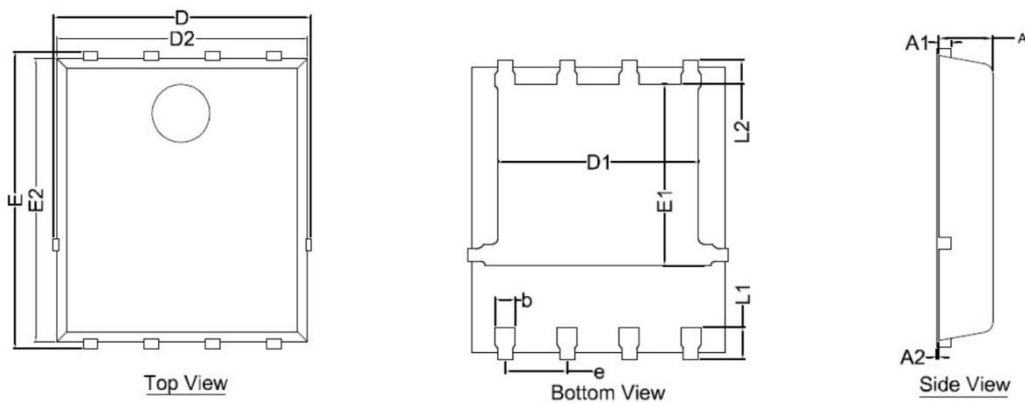


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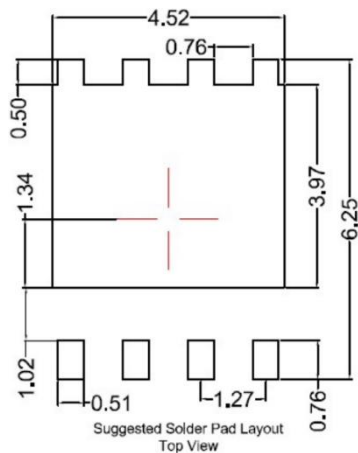
Package Information

PDFN5*6-8L

Dimensions in mm



Symbol	Millimeter		
	Min	Nom	Max
D	5.15	5.35	5.55
E	5.95	6.15	6.35
A	1.00	1.10	1.20
A1	0.254 BSC		
A2			0.10
D1	3.92	4.12	4.32
E1	3.52	3.72	3.92
D2	5.00	5.20	5.40
E2	5.66	5.86	6.06
L1	0.56	0.66	0.76
L2	0.50 BSC		
b	0.31	0.41	0.51
e		1.27 BSC	



Note:

1. Controlling dimension: in millimeters
2. General tolerance: $\pm 0.10\text{mm}$
3. The pad layout is for reference purpose only



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