

Q-SSC05120-T

Silicon Carbide Schottky Barrier Diode

1200V, 5A SiC SBD

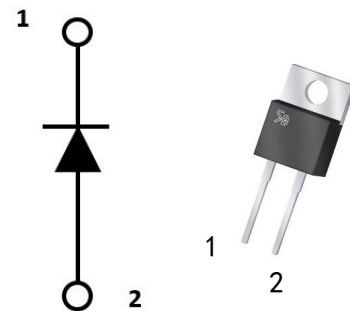
Features

- Temperature independent switching behavior
- No reverse recovery current / No forward recovery
- Excellent thermal performances
- High surge current capability

V_{RRM}	1200V
I_F	5A (TC=141°C)
Q_C	24nC

Benefits

- Higher frequency
- Low heat dissipation requirements
- Reduce size and cost of the system
- High-reliability



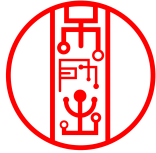
Applications

- Solar inverter / Renewable energy applications
- Uninterruptible power supply
- Power factor correction
- Motor drives

Marking	Package
Q-SSC05120-T	TO-220AC

Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Repetitive Peak Reverse Voltage	V_{RRM}	1200	V
Continuous Forward Current, D=1	I_F	Tc=25°C 17.7	A
		Tc=141°C 5	
Non-Repetitive Peak Forward Surge Current, Half Sine Wave, 10ms	$I_{F,SM}$	Tc=25°C 51	A
		Tc=150°C 42.5	
i^2t Value, 10ms	$\int i^2 dt$	13	A
Non-Repetitive Peak Forward Current, 10us	$I_{F,max}$	306	A
Power Dissipation	P_D	90.8	W
Storage Temperature Range	T_{STG}	-55 to 150°C	°C
Operating Junction Temperature Range	T_J	-55 to 175°C	°C



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Thermal Characteristics

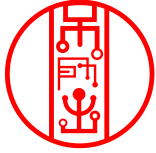
Parameter	Symbol	Conditions	Min.	Typ	Max	Unit
Maximum Junction-to-Ambient ¹	R _{thJA}	TO-220AC	-	1.18	1.65	°C/W
Maximum Junction-to-Case ¹	R _{thJC}	TO-220AC	-	-	60	°C/W

Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
STATIC CHARACTERISTICS						
DC Blocking Voltage	V _R	I _R =100uA, T _j =25°C	1200	-	-	V
		I _R =100uA, T _j =175°C	1200	-	-	
Forward Voltage	V _F	I _F =5A, T _j =25°C	-	1.4	1.8	V
		I _F =5A, T _j =150°C	-	1.8	2.2	
		I _F =5A, T _j =175°C	-	1.9	2.4	
Reverse Current	I _R	V _R =1200V, T _j =25°C	-	1	50	μA
		V _R =1200V, T _j =150°C	-	10	100	
		V _R =1200V, T _j =175°C	-	15	200	
DYNAMIC CHARACTERISTICS						
Total Capacitive Charge	Q _C	V _R =800V, T _j =25°C $Q_C = \int_0^{V_R} C(V) dV$	-	24	-	nC
Total Capacitance	C	V _R =0.1V, f=1MHz, T _j =25°C	-	385	-	pF
		V _R =400V, f=1MHz, T _j =25°C	-	26	-	
		V _R =800V, f=1MHz, T _j =25°C	-	19	-	
Capacitance Stored Energy	E _C	V _R =800V, f=1MHz, T _j =25°C	-	12.4	-	μJ

Notes:

1. Heat sink size: 25 x 17 x 4 cm³
2. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
3. The power dissipation is limited by 175°C junction temperature.
4. The data is theoretically the same as I_F and I_{FSM} in real applications, should be limited by total power dissipation.



Typical Operating Characteristics

Figure 1: Typical Forward Characteristics

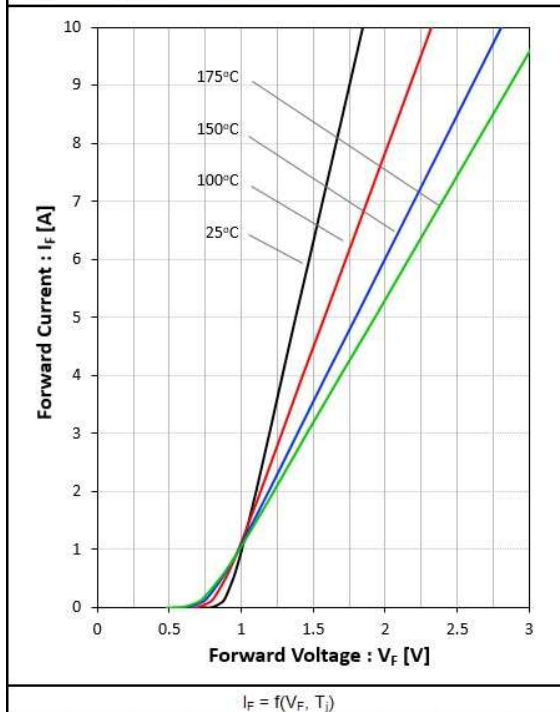


Figure 2: Typical Reverse Characteristics

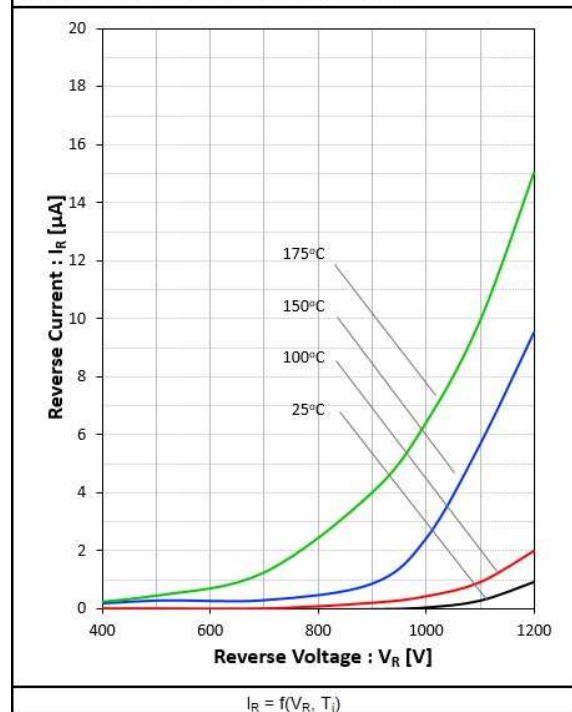


Figure 3: Power Derating Curves

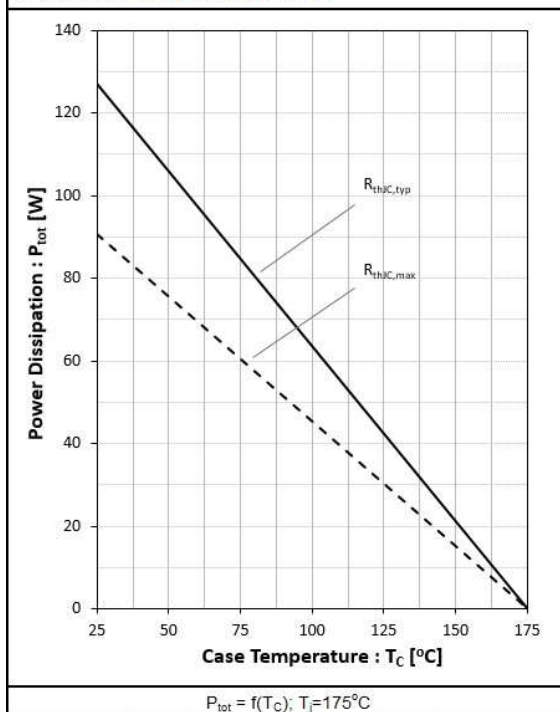
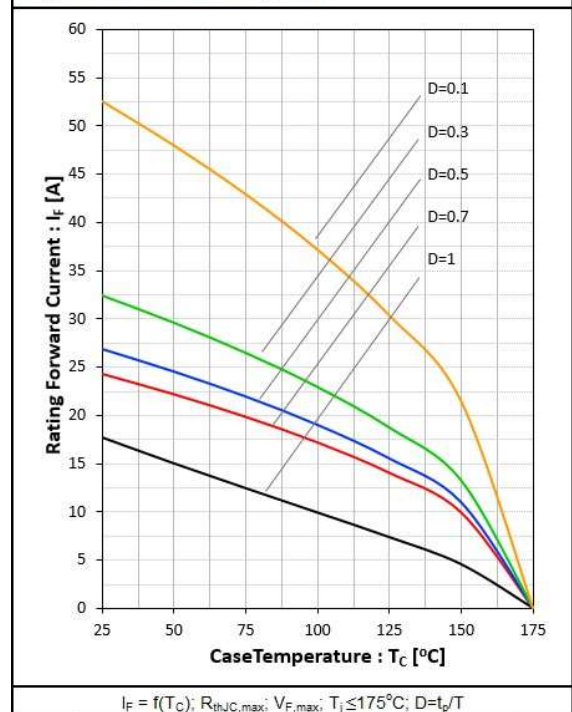
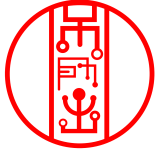


Figure 4: Current Derating Curves





Typical Operating Characteristics (Cont.)

Figure 5: Typical Junction Capacitance

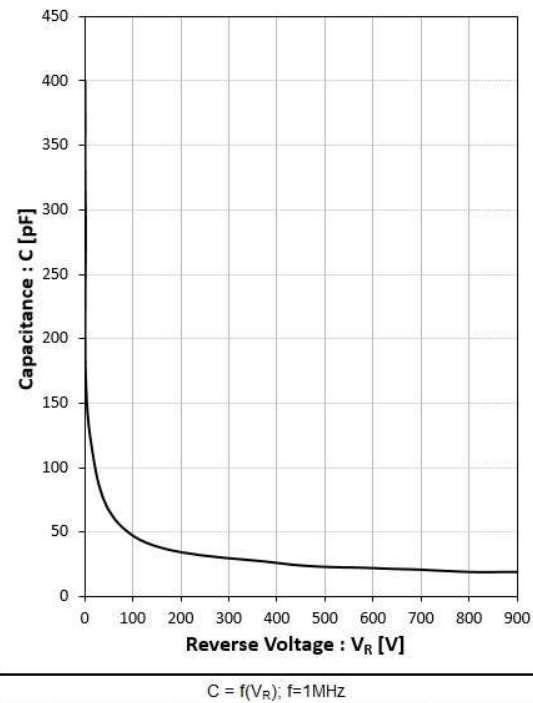


Figure 6: Typical Capacitive Charge

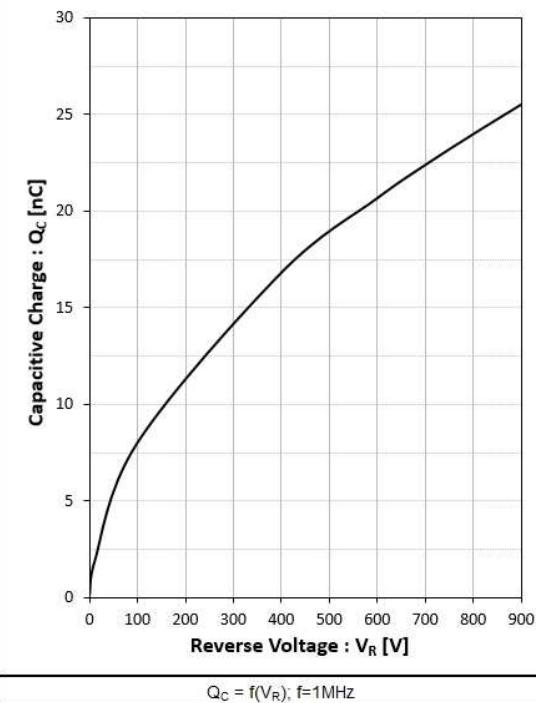


Figure 7: Typical Capacitive Energy

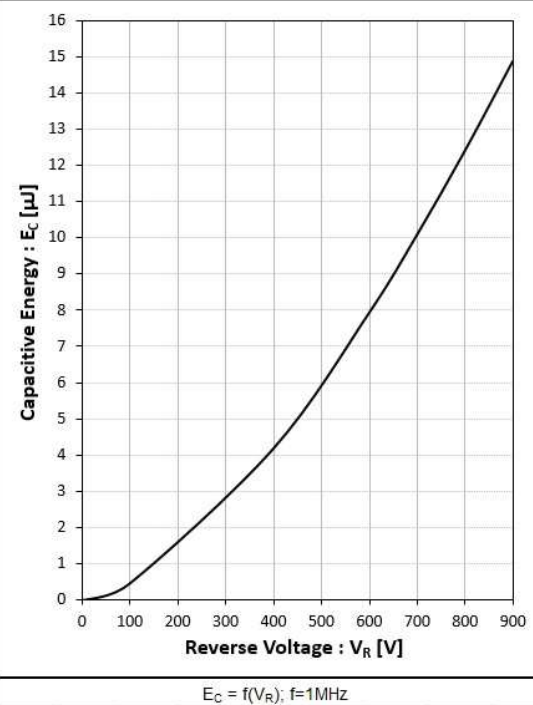
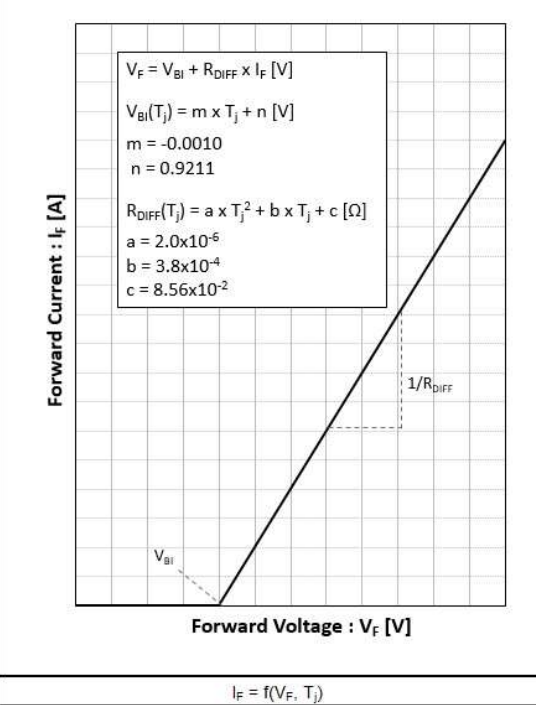
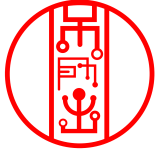


Figure 8: Forward Curve Model





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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

TO-220AC

